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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10067425	02/04/2002	438	126	2812	JEER

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**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:

EUROPEAN PATENT OFFICE (EPO) 01200496.6 02/09/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			NL 010069
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			
Verified and Acknowledged Examiners's initials			
TITLE : Method of manufacturing a semiconductor device and a semiconductor device obtained by means of said method			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sketch Dwg.	Figs. Dwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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